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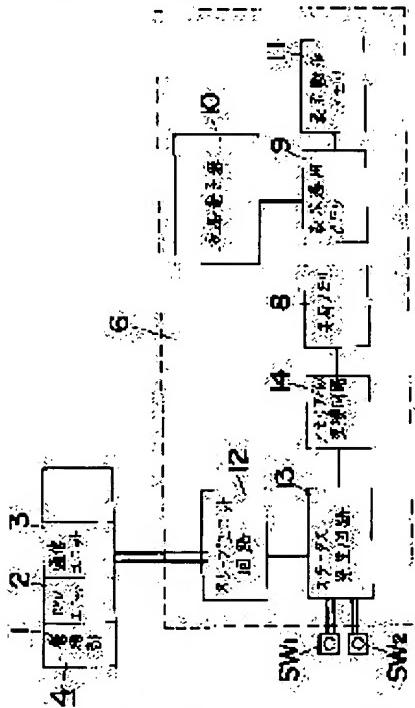
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## **(54) DISPLAY DEVICE FOR PROGRAMMABLE CONTROLLER**



### **(57)Abstract:**

**PURPOSE:** To provide the display device of a programmable controller which can perform high-speed, diverse control with an irreducible minimum number of I/O control points.

**CONSTITUTION:** A status generating circuit 13 can vary the number of control points of I/O by the settings of a switch SW1 for the number of input control points and a switch SW2 for the number of output control points and returns the status to a PC 4. A memory address converting circuit 14 converts the address of the status generating circuit 13 into the memory map of a common memory 8. A bit specification area formed in the memory map and an area where data in bit units of a key output area are written are assigned to the I/O and its capacity is varied with the number of the I/O control points.

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